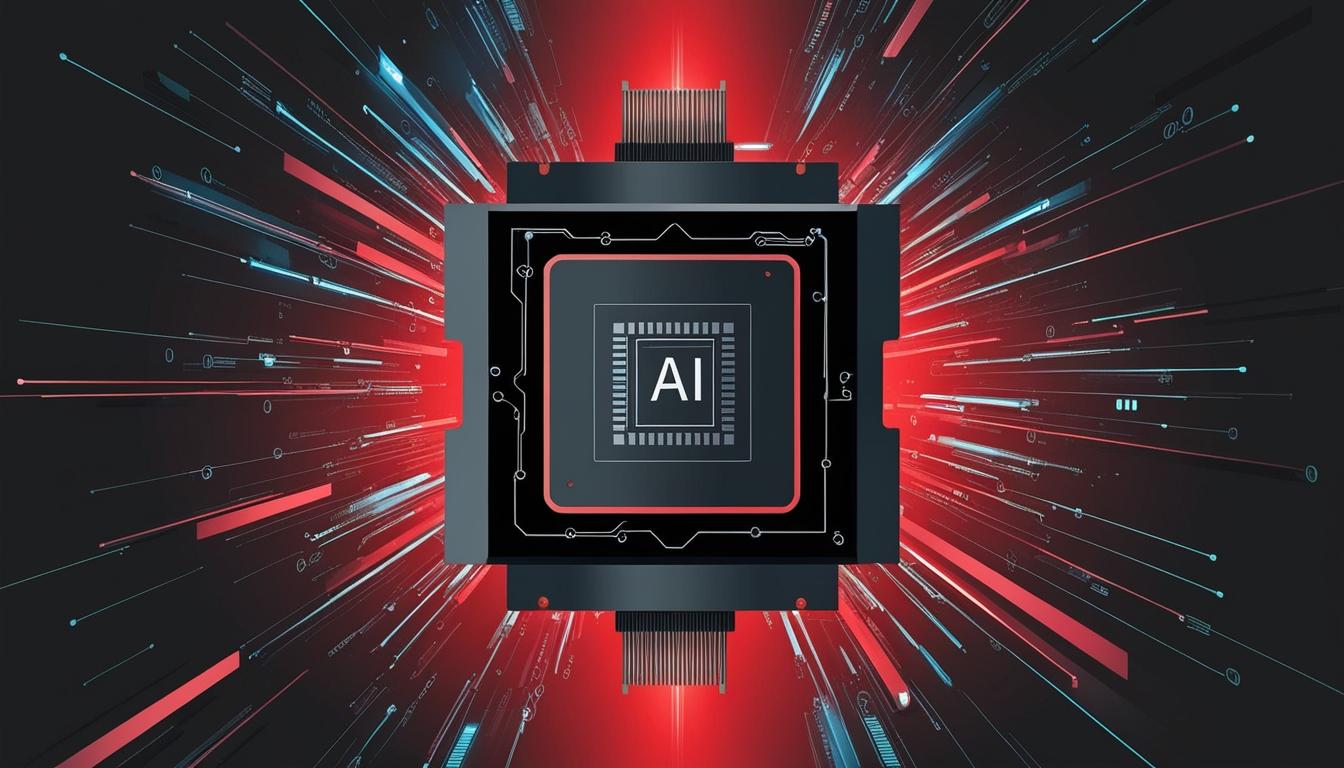
# MICROIP to unveil radical chip design platform at CES 2025



At the upcoming Consumer Electronics Show (CES) 2025, taking place from January 7 to 10 at the Las Vegas Convention Center, Taiwanese technology company MICROIP is set to make a significant impact with the introduction of its Rapid IC Design Platform. This unveiling marks MICROIP's inaugural participation in what is widely recognised as the world’s largest tech event.

The Rapid IC Design Platform aims to revolutionise the development timeline of computer chips, enabling the creation of specialized chips—such as those integral to tap-to-pay functionalities in smartphones—within a remarkable 12-month timeframe. Dr. James Yang, Chairman of MICROIP, expressed enthusiasm for the event, stating, “CES 2025 is the perfect stage to showcase our ASIC design expertise and AI software capabilities.” He further elaborated on the company’s offerings, highlighting their commitment to elevating industry standards and delivering unique value on a global scale.

In addition to the Rapid IC Design Platform, MICROIP plans to introduce its Artificial Intelligence (AI) Software Service Platform at CES. This platform collaborates with MediaTek’s Genio system to develop energy-efficient chips, particularly designed for industrial devices that require the quick processing of substantial data amounts. The infusion of advanced 6-nanometer technology from TSMC promises to enhance the operational capabilities of these chips.

The incorporation of AI into the chip design process offers notable efficiencies; MICROIP's AI system is capable of early issue detection and problem resolution, significantly reducing the time spent on testing and redesigning. This functionality not only accelerates the chip design process but also promotes the practice of sharing and reusing idle intellectual property (IP) among companies, which can ultimately lead to cost reductions and avoid redundant effort in new projects.

Further cementing its commitment to the future of chip design, MICROIP entered a partnership in October 2024 with the University of Foreign Languages and Information Technology (HUFLIT) in Ho Chi Minh City, Vietnam. This collaboration is focused on mentoring the next generation of chip designers by providing them with access to specialised computer boards, memory systems, and software necessary for mastering modern chip creation.

Looking ahead, MICROIP has ambitious plans to further develop AI-powered design tools tailored for diverse applications, including smart city initiatives, healthcare systems, and industrial automation. Attendees can view their innovative technologies at Booth #41608 in South Hall 3 of the Las Vegas Convention Center.

Source: [Noah Wire Services](https://www.noahwire.com)

## Bibliography

1. <https://www.prnewswire.com/news-releases/microip-debuts-at-ces-2025-showcasing-rapid-asic-design-and-ai-innovation-302340718.html> - Corroborates MICROIP's debut at CES 2025, the introduction of the Rapid IC Design Platform, and the collaboration with MediaTek on the Genio AI IoT platform.
2. <https://www.prnewswire.com/news-releases/microip-debuts-at-ces-2025-showcasing-rapid-asic-design-and-ai-innovation-302340718.html> - Details the Rapid IC Design Platform's ability to reduce the NFC chip development timeline to 12 months and the use of TSMC's 6nm NPU process.
3. <https://www.trendhunter.com/trends/ic-design> - Supports the introduction of the Rapid IC Design Platform and its impact on accelerating chip development timelines.
4. <https://www.trendhunter.com/trends/ic-design> - Explains the collaboration with MediaTek on the Genio AI IoT platform and the use of advanced 6-nanometer technology from TSMC.
5. <https://www.prnewswire.com/news-releases/microip-debuts-at-ces-2025-showcasing-rapid-asic-design-and-ai-innovation-302340718.html> - Describes the AI system's capability for early issue detection and problem resolution, reducing testing and redesign time.
6. <https://www.trendhunter.com/trends/ic-design> - Highlights the integration of AI-based EDA tools, such as Arculus System's iProfiler, to automate design processes and reduce development timelines.
7. <https://www.prnewswire.com/news-releases/microip-debuts-at-ces-2025-showcasing-rapid-asic-design-and-ai-innovation-302340718.html> - Quotes Dr. James Yang, Chairman of MICROIP, on the company's commitment to elevating industry standards and delivering unique value globally.
8. <https://www.prnewswire.com/news-releases/microip-debuts-at-ces-2025-showcasing-rapid-asic-design-and-ai-innovation-302340718.html> - Provides details on MICROIP's booth location at CES 2025 and their plans for expanding into Europe, the Americas, and Asia.
9. <https://cacm.acm.org/news/ai-reinvents-chip-design/> - Supports the broader trend of using AI in chip design to improve efficiency, reduce power consumption, and speed development cycles.
10. <https://www.trendhunter.com/trends/ic-design> - Outlines the industry implications of MICROIP's advancements, including the impact on semiconductor manufacturing and wireless communication technology.
11. <https://news.google.com/rss/articles/CBMiXEFVX3lxTE5xMmtVNmFCbzNfRkN6Wk04cHJONU1kZnpYMzU4VXRMSU5ERWpUWmcxenZ1QXJXd2ZVVGM4eEFIMnpJRTEwbmFXMkdPTmZaRktjdXhzck9IREYxNF82?oc=5&hl=en-US&gl=US&ceid=US:en> - Please view link - unable to able to access data